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								Applicant Shigeru YONEDA												
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U.S. PATENT DOCUMENTS																				
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FOREIGN PATENT DOCUMENTS																				
		Document Number							Date		Country				Class		Sub-class		Translation	
																			Yes No	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)																				
		H. Tanobe, et al., "Temperature Insensitive Arrayed Waveguide Gratings on InP Substrates", IEEE Photonics Technology Letters, Vol. 10, No.2, February 1998, pp. 235-237																		
		Y. Inoue, et al., "Athermal Silica-Based Arrayed-Waveguide Grating Multiplexer", Electronics Letters, Vol. 33, No. 23, Nov. 6, 1997, pp. 1945-1947																		
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